ATC500
Base Board for Wideband Massive MIMO Software Defined Radio

Key Features

- Dual Virtex UltraScale+ VU13P provides massive DSP power using over 24,000 FPGA DSP slices to deliver up to 12TMACs per second per board
- Backplane connections support full mesh fabric at 100G
- Network synchronization distributes reference and radio frame clocks across multiple ATC500 boards
- Built-in TX and RX reference for antenna port phase alignment and calibration to support massive MIMO
- Highly integrated patent-pending radio modules built using four ADRV9009 enhanced by RF DSPs advanced algorithms
- Each board supports dual high-performance radio modules and scalable to 12 boards with super low jitter among all RF ports

Benefits

- Integrated platform for Wideband MIMO Software Defined Radio
- Wideband TDD or FDD MIMO scalable from 4T4R to 128T128R and continuous signal bandwidth from 200 MHz to over 2 GHz of programmable modulation and waveform
- Airborne MIMO radio applications, satellite communication, ultra-wideband RF playback and recording, spectrum monitoring, fully digital phased array and direction finding
- Beamforming for highly efficient simultaneous jamming and secure communication
- Complete integrated solution available from a single vendor
- Electrical, mechanical, software, algorithm and system-level expertise in house
- AS9100 and ISO9001 certified company
**ATC500**

The ATC500 is an AdvancedTCA Blade whose patent-pending architecture supports a scalable and flexible integrated platform for Wideband Massive MIMO Software Defined Radio.

The two XCVU13P FPGAs provide 100GbE connections to the backplane for full mesh connectivity, plus 100GbE connections to the Zone 3 RTM for further I/O expansion. The FPGAs can be used to implement MIMO PHY processing including FFT, channel estimation, FEC En/Decode and MU-MIMO beamforming. Applications include:

- Airborne MIMO radio for secure, high reliability, high spectrum efficiency and high throughput wireless communication.
- Recording and playback of sixteen 200 MHz RF paths per blade.
- Zero-forcing coding to defeat eavesdropping
- Beamforming for highly efficient, simultaneous jamming and secure communication
- Fixed wireless access by massive MIMO spatial multiplexing
- 5G/LTE/4G networks

The ATC500 is also excellent for ASIC verification, AI-Machine learning inference, configurable and dynamically allocable DSP processing for Software Defined Network (SDN).

When fully populated, each ATC500 supports 16T16R TDD or 8T8R FDD, frequency agile from 300 MHz to 6 GHz. On-board synchronization, calibration circuits and algorithms support phase alignments across all RF ports, which can be extended across multiple carriers to provide over 2 GHz bandwidth of modulation and waveform.

When used in a 6-slot full mesh chassis (such as VT830) the system can be scaled up to 64T64R with only four blade or 192T192R with 12 blades in the system such as VadaTech VT820/VT822/VT825. It supports sub 6 GHz bands out-of-box and can function as the IF to support mmwave with custom radio frontend, with bandwidth from 400 MHz to over 2 GHz.

RF DSP Inc [www.rfdsp.com](http://www.rfdsp.com) can provide a 5G NR MIMO Radio unit which supports ORAN/XRAN flexible PHY partition with FEC encoding/decoding and DSP offload that features:

- From 4T4R and 8T8R small cell to massive MIMO 64T64R and 128T128R macro cell.
- Multi-Channel signal generators and signal analyzers up to 2 GHz modulation bandwidth from 8 - 192 channels.
- Massive Data Acquisition Systems in one compact form factor with full scalability.

The ATC500 supports two Radio Modules (each has four ADRV9009) and can be configured with single or dual FPGAs. It has an option for a Rear Transition Module (RTM), ART500A. The modules provide QSFP28 ports, GPS receiver, GbE, and other I/O via the rear. Please consult the ART500 data sheet for more information.

The ATC500 implements IPMI 2.0 for its management and payload.
System and Firmware

We provide complete, flexible and scalable complete systems including ACTA chassis, ATC500 blades with highly integrated MIMO radio modules, Xeon computing blades, storage blades and system management blades. In cooperation with RF DSP Inc. (www.rfdsp.com), available firmware and software options include

- Radio clock sync and IQ interface
- TDD and FDD configuration management
- Expanded bandwidth to over 2 GHz
- MIMO channel estimation
- RF playback and recording
- Multi-channel signal generator and analyzer
- Massive Data Acquisition Systems
- MIMO calibration and antenna phase alignment
- Multi-User MIMO (MU-MIMO) beamforming
- Digital phased array and direction finding
- Advanced precoding for highly efficient simultaneous jamming and communication, etc.
- These firmware and software options are scalable from 4T4R to 128T128R and from 200 MHz to over 2 GHz contiguous signal bandwidth
5G NR MIMO Radio Unit

- eNB / gNB
  - = L3/L2 + PHY split
  - X-RAN interface to RU

- RF DSP RU
  - 100 MHz 5G NR, sub 6 GHz, with Carrier Aggregation
  - 8T/8R, 16T/16R or 64T/64R TDD or FDD
  - XRAN Category A capable, plus RF DSP proprietary time domain interface support within X-RAN frame structure

Data Interface:
- Ethernet 40Gbit/s or 100Gbit/s (option for 2nd 40/100G link)
- Config/Status Interface: Ethernet 1Gbit/s
- Debug Interface: (also config/status) UART 115.2kbit/s

RF DSP RU (Radio Unit)
- 16 antenna connectors

FEC encoding/decoding & FFT Engine included

Figure 2: 5G NR MIMO Radio Unit

Test-Integrated Complete Development Platform

5G NR MIMO RU Total Solution Provider:
Complete RU + XRAN interface (extendable) + MIMO channel emulator for testing and optimization

1) RU component verification
  - first with digital bit exact

2) System Test Platform
  - using RF DSP MIMO channel emulator

Figure 3: Test-Integrated Complete Development System
Block Diagram

Figure 4: ATC500 Functional Block Diagram

Front Panel

Figure 5: ATC500 Front Panel
# Specifications

<table>
<thead>
<tr>
<th><strong>Architecture</strong></th>
<th><strong>Dimensions</strong></th>
<th>Width: 12.69&quot; (322.25 mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Type</strong></td>
<td><strong>ATCA MIMO Radio</strong></td>
<td>Massive MIMO SDR</td>
</tr>
<tr>
<td><strong>Standards</strong></td>
<td><strong>Module Management</strong></td>
<td>IPMI v2.0 and PICMG 3.0</td>
</tr>
<tr>
<td><strong>10GbE</strong></td>
<td><strong>IPMI</strong></td>
<td>IPMI v2.0 and PICMG 3.0</td>
</tr>
<tr>
<td><strong>GbE</strong></td>
<td><strong>KR4/XAUI</strong></td>
<td>100/40/10GbE to the fabric channel</td>
</tr>
<tr>
<td><strong>Type</strong></td>
<td><strong>FPGA</strong></td>
<td></td>
</tr>
<tr>
<td><strong>PICMG</strong></td>
<td><strong>ATCA</strong></td>
<td>PICMG 3.0 R3.0</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th><strong>Configuration</strong></th>
<th><strong>Power</strong></th>
<th>ATC500 ~250W</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Environmental</strong></td>
<td><strong>Temperature</strong></td>
<td>See Ordering Options</td>
</tr>
<tr>
<td></td>
<td>Storage Temperature: −40° to +85°C</td>
<td></td>
</tr>
<tr>
<td><strong>Vibration</strong></td>
<td>1G, 5 to 500 Hz each axis</td>
<td></td>
</tr>
<tr>
<td><strong>Shock</strong></td>
<td>30G on each axis</td>
<td></td>
</tr>
<tr>
<td><strong>Relative Humidity</strong></td>
<td>5 to 95% non-condensing</td>
<td></td>
</tr>
<tr>
<td><strong>Front Panel</strong></td>
<td><strong>Interface Connectors</strong></td>
<td>USB, RS-232, JTAG, etc.</td>
</tr>
<tr>
<td></td>
<td><strong>LEDs</strong></td>
<td>Activity/Link, User defined and health management</td>
</tr>
<tr>
<td></td>
<td></td>
<td>IPMI Management Control</td>
</tr>
</tbody>
</table>

| **Software Support** | **Operating System** | Linux |

<table>
<thead>
<tr>
<th><strong>Other</strong></th>
<th><strong>MTBF</strong></th>
<th>MIL Hand book 217-F@ TBD hrs</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Certifications</strong></td>
<td>Designed to meet FCC, CE and UL certifications, where applicable</td>
<td></td>
</tr>
<tr>
<td><strong>Standards</strong></td>
<td>VadaTech is certified to both the ISO9001:2000 and AS9100B:2004 standards</td>
<td></td>
</tr>
<tr>
<td><strong>Warranty</strong></td>
<td>Two (2) years, see VadaTech Terms and Conditions</td>
<td></td>
</tr>
</tbody>
</table>

**INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS**

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.
Ordering Options

ATC500 – A00-D00-00J

<table>
<thead>
<tr>
<th>A = Number of FPGAs</th>
<th>D = FPGA Speed</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 = Two VU13P</td>
<td>1 = High (-2)*</td>
</tr>
<tr>
<td>1 = One VU13P</td>
<td>2 = High (-2LE)</td>
</tr>
<tr>
<td></td>
<td>3 = Highest (-3E)*</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>J = Temperature Range and Coating</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 = Commercial (−5° to +55°C), No coating</td>
</tr>
<tr>
<td>1 = Commercial (−5° to +55°C), Humiseal 1A33 Polyurethane</td>
</tr>
<tr>
<td>2 = Commercial (−5° to +55°C), Humiseal 1B31 Acrylic</td>
</tr>
<tr>
<td>3 = Industrial (−20° to +70°C), No coating</td>
</tr>
<tr>
<td>4 = Industrial (−20° to +70°C), Humiseal 1A33 Polyurethane</td>
</tr>
<tr>
<td>5 = Industrial (−20° to +70°C), Humiseal 1B31 Acrylic</td>
</tr>
<tr>
<td>6 = Extended (−40° to +85°C), Humiseal 1A33 Polyurethane</td>
</tr>
<tr>
<td>7 = Extended (−40° to +85°C), Humiseal 1B31 Acrylic</td>
</tr>
</tbody>
</table>

Notes: *Minimum Order Quantity applies for these FPGA SKU’s.
For operational reasons VadaTech reserves the right to supply a higher speed FPGA device than specified on any particular order/delivery at no additional cost, unless the customer has entered into a Revision Lock agreement with respect to this product.

Related Products

- ATCA Processing Carrier with a standard PCIe edge Module
- Xeon E3-1268L V3 Processor with 32 GB ECC
- Quad Core @ 2.3 GHz or Turbo Frequency @ 3.3 GHz

- 10G ATCA Carrier
- Xilinx Virtex-7 FPGA (XC7V690T in FFG1761 package)
- Crossbar switch to connect FPGA to full mesh of backplane fabric

- 19” rackmount 6U ATCA Chassis with integrated Switch and Shelf Manager
- 10GbE/GbE Managed Layer 2
- 40GbE/10GbE/GbE Managed Layer 3