

VTX660

8U VPX Chassis, Six 3U Slots with RTM Support



VTX660

Key Features

- 8U Open VPX rackmount system platform
- Dedicated Switch/management slot
- Up to five 3U VPX payload slots
- Compatible with 0.8-inch, 0.85-inch and 1.0-inch modules
- Support for Rear Transition Modules (RTMs)
- Redundant cooling in push/pull front-to-back airflow configuration
- Optional JTAG Switch Module (JSM)

Benefits

- Dual 800 W AC or 650 W DC Power Supply
- Electrical, mechanical, software, and system-level expertise in house
- Full system supply from industry leader
- AS9100 and ISO9001 certified company



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VTX660

The VTX660 is an 8U VPX chassis with six 3U VPX slots. The chassis can accept 0.8-inch, 0.85-inch and 1.0-inch pitch modules. The chassis is ideal for commercial deployment.

Power Supplies

The VTX660 has dual AC input power supplies to provide 800 W with redundancy (1+1) or DC -48 V input 650 W. The chassis supplies 95 W/slot and the AC input is universal.

Cooling and Temperature Sensors

The VTX660 provides cooling to the VPX slots designed to meet ANSI/VITA 65 providing 18 CFM per slot at 0.24 in-H₂O @ 5000 feet. The VTX660 provides push/pull cooling to the RTM slots.

Backplane

The backplane provides five 3U VPX payload slots in a star configuration, fully compliant to VITA 46.0 baseline specification with additional support to the RTMs, compliant to VITA 46.10 and OpenVPX VITA 65.

JSM

There is an optional JTAG Switch Module to provide JTAG access to the front.



Figure 1: VTX660 Front View



Figure 2: VTX660 Rear View

Backplane Connections

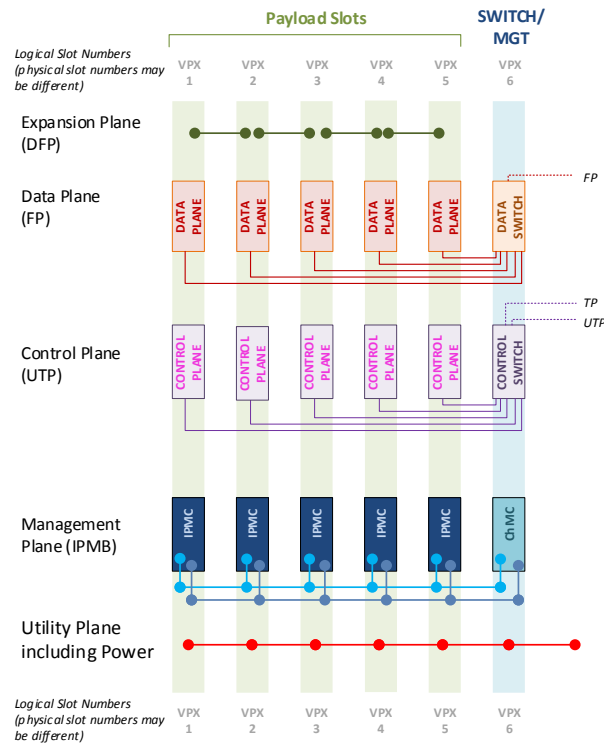


Figure 3: VTX660 Backplane Connections

The initial offering on VTX660 is based on backplane profile BKP3-CEN06_15.2.2-n. VadaTech can also design additional VITA standard backplane profiles for customer specific applications. Please contact your local sales team for more information.

Chassis Layout



Figure 4: VTX660 Chassis Layout - Front



Figure 5: VTX660 Chassis Layout - Rear

Specifications

Architecture	
Physical	Dimensions Height: 8U Width: 8.5" Depth: 12.5" Weight: TBD
Type	VPX Shelf 5 Payload Slot up to 1.0" pitch and a dedicated Switch/management slot
Standards	
VPX	Type VITA 46.0 Baseline Specification
Configuration	
Power	VTX660 800 W AC universal input, dual power supply with redundancy (or -48 V DC 650 W)
Environmental	See ordering options
Cooling	Front to Back
Other	
MTBF	MIL Hand book 217-F@ TBD hrs
Certifications	Designed to meet FCC, CE and UL certifications, where applicable
Standards	VadaTech is certified to both the ISO9001:2000 and AS9100B:2004 standards
Warranty	Two (2) years

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.

Ordering Options

VTX660 – A00-D00-0HJ

A = Power Supply	D = JSM	
0 = Single AC (800 W) 1 = Dual AC (1+1, 1600 W) 2 = Single DC -48V (650 W) 3 = Dual DC -48V (1+1, 1300 W)	0 = No JSM 1 = JSM	
		H = Environmental
		See Environmental Specification Table below
		J = Conformal Coating
		0 = No coating 1 = Humiseal 1A33 polyurethane 2 = Humiseal 1B31 acrylic

Environmental Specification*

Option H	H = 0	
Operating Temperature	-5°C to +55°C	
Storage Temperature	-40°C to +85°C	
Operating Vibration	0.04 g2/Hz max	
Storage Vibration	20g	
Humidity	95% non-condensing	

Notes: *Please contact VadaTech Sales for other specification

Related Products

VPX518



- AMC FPGA carrier for FMC per VITA 57
- Xilinx Zynq-7000 FPGA in FFG-900 package (XC7Z100 or XC7Z045) with embedded ARM®
- Supported by DAQ Series™ data

VPX592



- 3U FPGA carrier for FPGA Mezzanine Card (FMC) per VITA 46 and VITA 57
- Xilinx Kintex UltraScale™ XCKU115 FPGA
- High-performance clock jitter cleaner

VPX599



- 3U FPGA Dual DAC and dual ADC per VITA 46
- Xilinx Kintex UltraScale™ XCKU115 FPGA
- Dual ADC @ 6.4 GSPS 12-bits

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